

ZXTC2063E640V, SOT23-6, complementary medium power transistors

Summary

 $BV_{CEO} > 40 (-40)V$

 $BV_{ECO} > 6 (-3)V$

 $I_{C(cont)} = 3.5 (-3)A$

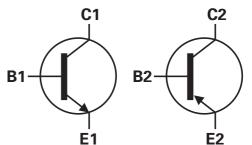
 $V_{CE(sat)} < 60 (-90) mV @ 1A$

 $R_{CE(sat)} = 38 (58) m\Omega$

 $P_{D} = 1.1W$



Advanced process capability has been used to achieve this high performance device. Combining NPN and PNP transistors in the SOT23-6 package provides a compact solution for the intended applications.

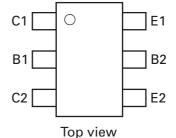


Features

- · NPN PNP combination
- · Very low saturation voltage
- · High gain
- SOT23-6 package

Applications

- · MOSFET and IGBT gate driving
- · Motor drive



Ordering information

Device	reel size (inches)	Tape width (mm)	Quantity per reel
ZXTC2063E6TA	7	8	3000

Device marking

2063

Absolute maximum and thermal ratings

Parameter	Symbol	Limit	Unit
Collector-base voltage	V _{CBO}	130(-45)	V
Collector-emitter voltage	V _{CEO}	40(-40)	V
Emitter-collector voltage (reverse blocking)	V _{ECO}	6(-3)	V
Emitter-base voltage	V _{EBO}	7(-7)	V
Continuous collector current ^{(c)(f)}	I _C	3.5(-3)	Α
Peak pulse current	I _{CM}	9(-9)	Α
Base current	I _B	1(-1)	Α
Power dissipation @ T _{amb} = 25°C ^{(a)(f)}		0.7	W
Linear derating factor	P_{D}	5.6	mW/°C
Power dissipation @ T _{amb} = 25°C ^{(b)(f)}		0.9	W
Linear derating factor	P_{D}	7.2	mW/°C
Power dissipation @ T _{amb} = 25°C ^{(b)(g)}		1.1	W
Linear derating factor	P _D	8.8	mW/°C
Power dissipation @ T _{amb} = 25°C ^{(c)(f)}		1.1	W
Linear derating factor	P_{D}	8.8	mW/°C
Power dissipation @ T _{amb} = 25°C ^{(d)(f)}		1.7	W
Linear derating factor	P _D	13.6	mW/°C
Operating and storage temperature range	T _j , T _{stg}	-55 to +150	°C
Thermal resistance junction to ambient ^{(a)(f)}	$R_{\Theta JC}$	179	°C/W
Thermal resistance junction to ambient ^{(b)(f)}	$R_{ heta JA}$	139	°C/W
Thermal resistance junction to ambient ^{(b)(g)}	$R_{ ext{ hetaJC}}$	113	°C/W
Thermal resistance junction to ambient ^{(c)(f)}	$R_{\Theta JC}$	113	°C/W
Thermal resistance junction to ambient ^{(d)(f)}	$R_{\Theta JA}$	73	°C/W

NOTES:

⁽a) For a device surface mounted on 15mm x 15mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

⁽b) For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

⁽c) For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions.

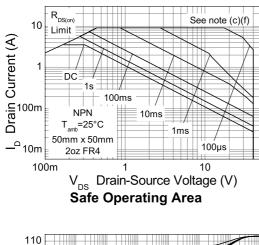
⁽d) As above measured at t<5 seconds.

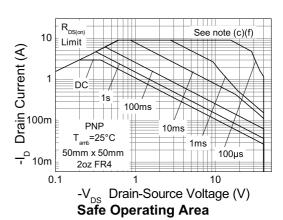
⁽e) Repetitive rating - pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.

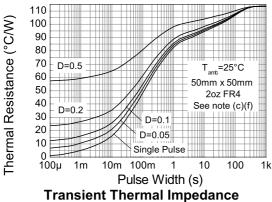
⁽f) For device with one active die, both collectors attached to a common sink.

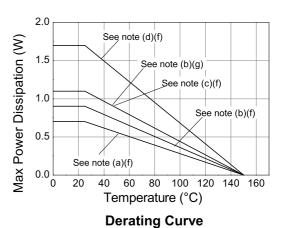
⁽g) For device with two active dice running at equal power, split sink 50% to each collector.

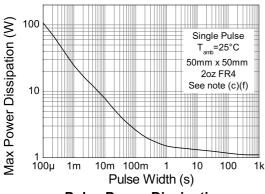
Thermal characteristics











Electrical characteristics (at $T_{amb} = 25$ °C unless otherwise stated)

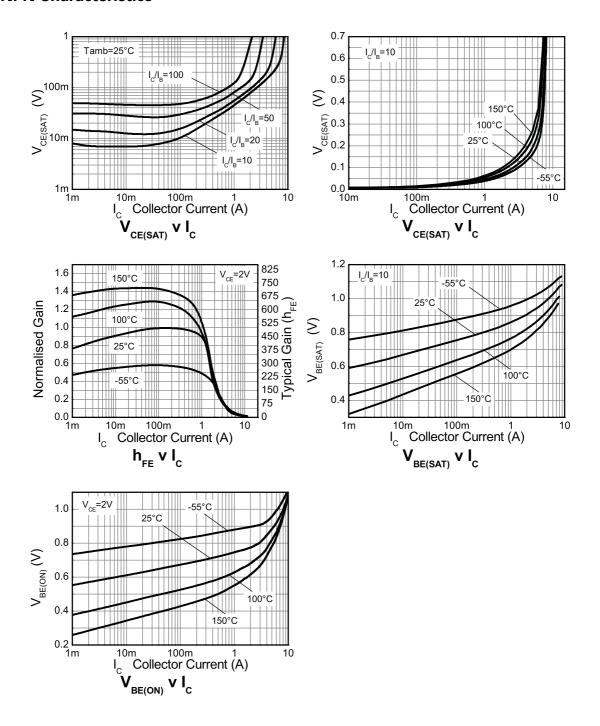
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV _{CBO}	130(-45)	170(-80)		V	$I_C = (-)100 \mu A$
Collector-emitter breakdown voltage (base open)	BV _{CEO}	(-)40	63(-65)		V	I _C = (-)10mA ^(*) *
Emitter-base breakdown voltage	BV _{EBO}	(-)7	(-)8.3		V	$I_E = (-)100 \mu A$
Emitter-collector breakdown voltage (reverse blocking)	BV _{ECX}	(-)6	(-)7.4		V	$\begin{split} I_E &= ()100 \mu\text{A, R}_{BC} < 1 \text{k}\Omega \text{ or} \\ 0.25 \text{V} &> \text{V}_{BC} > \text{-0.25V} \\ (0.25 \text{V} &< \text{V}_{BC} < \text{-0.25V}) \end{split}$
Emitter-collector breakdown voltage (base open)	BV _{ECO}	6(-3)	7.4(-8.7)		V	I _E = (-)100μA
Collector-base cut-off	I _{CBO}		<1	(-)50	nA	V _{CB} =100(-36)V
current				(-)20	μΑ	$V_{CB} = 100(-36)V$, $T_{amb} = 100^{\circ}C$
Emitter-base cut-off current	I _{EBO}		<1	(-)50	nA	V _{EB} = (-)5.6V
Collector-emitter	V _{CE(sat)}		50(-70)	60(-90)	mV	I _C = (-)1A, I _B = (-)100mA *
saturation voltage			85(-195)	110(-290)	mV	$I_C = (-)1A$, $I_B = (-)20mA *$
			150	220	mV	I _C = 2A, I _B = 40mA *
			(-175)	(-260)	mV	$(I_C = -3A, I_B = -300mA *)$
			135	195	mV	I _C = 3.5A, I _B = 350mA *
Base-emitter saturation	V _{BE(sat)}		(-935)	(-1000)	mV	$(I_C = -3A, I_B = -300mA *)$
voltage			960	1050	mV	I _C = 3.5A, I _B = 350mA *
Base-emitter turn-on	V _{BE(on)}		(-855)	(-950)	mV	$(I_C = -3A, V_{CE} = -2V *)$
voltage			860	950	mV	$I_C = 3.5A$, $V_{CE} = 2V *$
Static forward current	h _{FE}	()300	()450	()900		$I_C = (-)10 \text{mA}, V_{CE} = (-)2V *$
transfer ratio		280(200)	400(280)			$I_C = (-)1A, V_{CE} = (-)2V *$
		(20)	(50)			$(I_C = -3A, V_{CE} = -2V *)$
		40	60			$I_C = 3.5A$, $V_{CE} = 2V *$
Transition frequency	f _T		190 (270)		MHz	$I_C = (-)50 \text{mA}, V_{CE} = (-)10 \text{V}$ f = 100MHz
Output capacitance	C _{OBO}		12(17)	20(25)	pF	V _{CB} = (-)10V, f = 1MHz *
Delay time	t _d		64(57)		ns	$V_{CC} = (-)10V. I_C = (-)1A, I_{B1}$
Rise time	t _r		108(69)		ns	= I _{B2} = (-)10mA.
Storage time	t _s		428(154)		ns	
Fall time	t _f		130(60)		ns	

NOTES

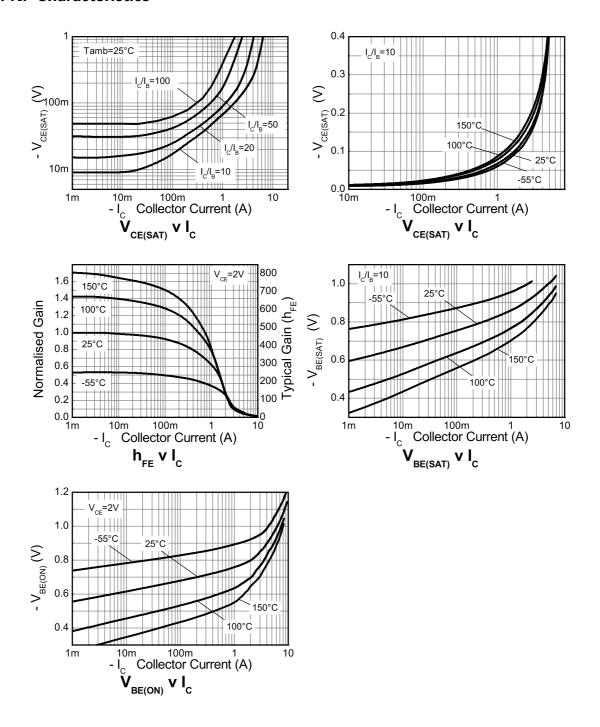
(*) Measured under pulsed conditions. Pulse width ${\leq}300\mu\text{s};$ duty cycle ${\leq}2\%$

() = PNP

NPN Characteristics

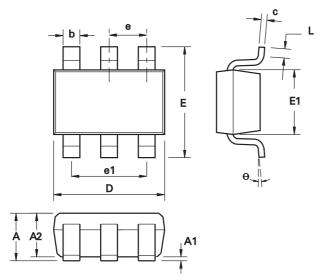


PNP Characteristics



ZXTC2063E6

Package outline - SOT23-6



DIM	Millin	neters	Inches		
	Min.	Max.	Min.	Max.	
Α	0.90	1.45	0.354	0.0570	
A1	0.00	0.15	0.00	0.0059	
A2	0.90	1.30	0.0354	0.0511	
b	0.35	0.50	0.0078	0.0196	
С	0.09	0.26	0.0035	0.0102	
D	2.70	3.10	0.1062	0.1220	
E	2.20	3.20	0.0866	0.1181	
E1	1.30	1.80	0.0511	0.0708	
L	0.10	0.60	0.0039	0.0236	
е	0.95 REF		0.0374 REF		
e1	1.90	1.90 REF		0.0748 REF	
L	0°	30°	0°	30°	

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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